



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of  
JOHN PETRUZELLO ET AL

Atty. Docket

US 010320

Serial No. 09/894,083

Group Art Unit: 2826

Filed: JUNE 28, 2001

Examiner: A. SEFER

*Handwritten:* #16  
Electronics/A  
*[Signature]*

Title: HV-SOI LDMOS DEVICE WITH INTEGRATED DIODE TO IMPROVE  
RELIABILITY AND AVALANCHE RUGGEDNESS

Commissioner for Patents  
Washington, D.C. 20231

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AMENDMENT

Sir:

In response to the official Office Action dated April  
22, 2002, please amend the above-identified application as  
follows:

IN THE CLAIMS

Kindly cancel Claims 10-14, 17 and 18 without  
prejudice.


REMARKS

In the outstanding Official Action, restriction was  
required as between Claims 1-9, 15 and 16 drawn to a Device.

In response, Applicants hereby elect Claims 1-9, 15  
and 16, drawn to the Device, for an examination on the merits,  
without prejudice to Applicants' right to subsequently file a  
divisional application directed to the nonelected method  
claims.

A favorable action on the merits is earnestly  
solicited.

Respectfully submitted,

By   
Steven R. Biren, Reg. 26,531  
Attorney  
(914) 333-9630

**CERTIFICATE OF MAILING**

It is hereby certified that this correspondence is being deposited with  
the United States Postal Service as first-class mail in an envelope addressed to:  
**COMMISSIONER OF PATENTS AND TRADEMARKS**  
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On <sup>21</sup>5/20/02 <sup>EB</sup>  
By 